

**MESSAGING SYSTEM FOR A PACKET TRANSPORT SYSTEM
AND METHOD OF OPERATION THEREOF**

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AND METHOD OF OPERATION THEREOF**

TECHNICAL FIELD OF THE INVENTION

The present invention is directed, in general, to packet
5 transport systems and, more specifically, to a messaging system
employable in a packet transport system.

BACKGROUND OF THE INVENTION

Packet transport systems employ a technique of disassembling
information at a sending end of a switching network for insertion
10 into separate packets of data and reassembling the same information
from the data packets at a receiving end of the switching network.
Communications systems employing this technique are especially
useful in common carrier or time-shared switching networks, since
a communication path or circuit required for packet transmission
15 associated with a user's message is needed only while each packet
is being forwarded through the switching network. The
communication path is, therefore, available to other users during
intervening periods.

Packet transport systems are capable of providing integrated information transport services for a wide range of applications (e.g., interactive data, bulk data, signaling, packetized voice, image). Instead of designing specialized networks optimized for specific applications, many services can be simultaneously supported over the same connection to the switching network. User information of varying types is converted into packets. The switching network transports these packets between users. End users are not tied to fixed rate connections. Instead, the switching network adapts the connection rates to the particular needs of the end users. Furthermore, it is possible to create a uniform user-network interface that is applicable to a broad range of services. Different applications may require different grades of service from the switching network. For example, packetized voice transmissions may possess very stringent delay requirements for delivery of the packets associated with an ongoing voice conversation, thus providing the users with acceptable quality of service.

A packet transport system generally includes a number of devices, wherein one device may be designated as a slave device that provides an interface to the switching network. A second device may be designated as a master device that provides the data (in the form of packets) to the slave device and performs buffer

management of the slave device. A problem may arise when the slave device depletes the data at a rate different from that expected by the master device. This may cause exception conditions to occur. The exception conditions may include overflow, underflow, abort of transmission or loss of data.

While the slave device typically runs at a line rate, i.e., a rate of a physical device of the switching network, the master device typically runs at a rate determined by a system clock thereof. The line rate and the rate (or frequency) of the system clock may be derived from the same source or from different sources. In either case, variations in the line rate or the frequency of the system clock, over time, may cause the slave device and the master device to run at different rates. Additionally, some protocols (e.g., the High-Level Data Link Control (HDLC) protocol) may require the slave device to insert control bits, stuffing bits, or error checking bits such that the amount of data transmitted by the slave device is greater than the amount of data provided to the slave device by the master device.

To accommodate the varying rates, some currently available packet transport systems employ separate interfaces designed to carry information that is necessary for communication between the various devices within the system. In systems having only a small, limited number of channels (for instance, 31 channels) a separate

indicator or line may be employed for each channel. Since each channel requires a separate line (with its attendant circuitry), this generally results in devices (e.g., slave and master devices) having increased pin count, circuit pack area and power consumption.

Other packet transport systems employ scheduling techniques, wherein multiple data packets are scheduled in advance for every data channel. This technique, however, does not discriminate between low and high priority packets and thus does not allow the higher priority packets to be transmitted first so as to minimize delay. Further, to avoid data underrun, data queues employed by the packet transport system may need to be filled with lower priority data packets. As a result, the higher priority data packets may be subject to unnecessary delays.

Still other packet transport systems completely eliminate the need for storage in the slave device by requiring the master device to send a limited number of bytes (usually between four and eight) to every channel in the slave device. The master device, however, has to poll the slave device at a much higher rate than that normally required. Additionally, the master device should monitor clock variations in the slave device, as well as the status of a buffer for each channel in the slave device.

Accordingly, what is needed in the art is a messaging system for communications between the slave device and the master device that overcomes the deficiencies of the prior art.

1. A messaging system for communications between a slave device and a master device, the messaging system comprising:
a. a master device; and
b. a slave device;
wherein the master device and the slave device are configured to communicate with each other via a messaging system.

SUMMARY OF THE INVENTION

To address the above-discussed deficiencies of the prior art, the present invention provides a messaging system for facilitating communications between a master device and a slave device of a packet transport system, and a method of operation thereof. The master device transmits packets to the slave device.

In one illustrative embodiment of the present invention, the messaging system includes (1) a channel level detector that reads a level of a first-in, first-out (FIFO) buffer of the slave device and compares the level to a threshold, and (2) an event driven message generator that issues an event driven message to the master device when the level reaches the threshold. The master device may thus adjust a rate at which the master device provides the packets to the slave device based on the event driven message to avoid an exception condition.

In another embodiment of the present invention, the messaging system includes (1) an aggregate level detector that determines storage levels of a plurality of channels associated with the slave device and (2) a periodic message generator that periodically issues to the master device a periodic message indicating the storage levels. The messaging system may thus provide the master

device with status of at least a group of channels associated with the slave device.

Order	1 st	2 nd	3 rd	4 th	5 th	6 th	7 th	8 th	9 th	10 th	11 th	12 th	13 th	14 th	15 th	16 th	17 th	18 th	19 th	20 th	21 st	22 nd	23 rd	24 th	25 th	26 th	27 th	28 th	29 th	30 th	31 st	32 nd	33 rd	34 th	35 th	36 th	37 th	38 th	39 th	40 th	41 st	42 nd	43 rd	44 th	45 th	46 th	47 th	48 th	49 th	50 th	51 st	52 nd	53 rd	54 th	55 th	56 th	57 th	58 th	59 th	60 th	61 st	62 nd	63 rd	64 th	65 th	66 th	67 th	68 th	69 th	70 th	71 st	72 nd	73 rd	74 th	75 th	76 th	77 th	78 th	79 th	80 th	81 st	82 nd	83 rd	84 th	85 th	86 th	87 th	88 th	89 th	90 th	91 st	92 nd	93 rd	94 th	95 th	96 th	97 th	98 th	99 th	100 th
1	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100

BRIEF DESCRIPTION OF THE DRAWINGS

For a more complete understanding of the present invention, reference is now made to the following descriptions taken in conjunction with the accompanying drawings, in which:

5 FIGURE 1 illustrates an embodiment of a packet switching network that provides an environment for the present invention; and

 FIGURE 2 illustrates an embodiment of a packet transport system constructed in accordance with the principles of the present invention.

DETAILED DESCRIPTION

Referring initially to FIGURE 1, illustrated is an exemplary embodiment of a packet switching network 100 that provides an environment for the present invention. The packet switching network 100 is arranged for establishing virtual circuit connections between terminal equipment, one of which is designated 110. The terminal equipment 110 transmit packets of data (containing information such as packetized voice) through connection lines, one of which is designated 120, to a packet multiplexer 130. While the terminal equipment 110 are illustrated as computer terminals, those skilled in the art understand that the terminal equipment 110 may include devices capable of operating with digitized voice, video or data.

A resulting output stream of packets, illustratively interspersed with one another, are transmitted from the packet multiplexer 130 over an access line 135 to a node 140 of the packet switching network 100. Other links (one of which is designated 150) also transmit streams of data packets into the node 140. Some of these links may originate at a multiplexer (such as the packet multiplexer 130), while others may originate at high speed terminal equipment. The packet switching network 100 typically includes a

large number of nodes 140, interconnected via a large number of links 150.

Illustrated is an exemplary virtual connection, shown by a heavily weighed path line linking one of the terminal equipment 110 to another of the terminal equipment 110. Transmission is illustratively bidirectional over such a virtual connection.

Turning now to FIGURE 2, illustrated is an exemplary embodiment of a packet transport system 200 constructed in accordance with the principles of the present invention. The illustrative packet transport system 200 includes a slave device 210 that provides a network interface to a packet switching network 205, which may be an asynchronous transfer mode (ATM) network. The packet transport system 200 further includes a master device 220 that provides data, which may be in the form of transmit packets (one of which is designated 225), to the slave device 210 via a transmit interface 227. The master device 220 also manages a packet storage buffer 212 of the slave device 210 in an attempt to avoid exception conditions, such as overflow, underflow, abort of transmission or loss of data. The slave device 210 in turn provides data, which may be in the form of receive packets (one of which is designated 215), to the master device 220 via a receive interface 217.

The slave device 210 is coupled to the packet switching network 205 and receives clock signals (one of which is designated 207) therefrom. While the slave device 210 typically runs at a rate determined by the clock signals 207 from the packet switching network 205, the master device 220 may run at a rate determined by its own internal system clock. The clock signals 207 from the packet switching network 205 and the master device's 220 internal system clock may be derived from the same source or from different sources. In either case, variations can occur that will cause the slave device 210 and the master device 220 to run at different rates, which may result in the exception conditions previously discussed.

The packet transport system 200, therefore, further includes a messaging system 230 for facilitating communications between the master device 220 and the slave device 210. In the illustrated embodiment, the messaging system 230 includes both an event driven messaging subsystem 240 and a periodic messaging subsystem 270. Of course, in other embodiments, the messaging system 230 may include only one of the event driven messaging subsystem 240 or the periodic messaging subsystem 270 and remain well within the scope of the present invention.

The event driven messaging subsystem 240 includes a channel level detector 250 that, in the illustrated embodiment, is

associated with the slave device 210. The event driven messaging subsystem 240 further includes an event driven message generator 260, which, in the illustrated embodiment, is also associated with the slave device 210. The channel level detector 250 and the event driven message generator 260 may be incorporated within the slave device 210 or, alternatively, may be external to the slave device 210 and remain well within the scope of the present invention.

Further, while the illustrated embodiment only shows one channel level detector 250, those skilled in the pertinent art will realize that the event driven messaging subsystem 240 may employ a separate channel level detector 250 for each channel of the slave device 210 or, alternatively, may employ one channel level detector 250 for at least a group of channels of the slave device 210. In other embodiments, the messaging subsystem 240 may employ a channel level detector 250 for multiple slave devices 210. In either case, the per channel level detector 250 is configured to read a level of a first-in, first-out (FIFO) buffer (of the packet storage buffer 212) associated with a particular channel of the slave device 210. Likewise, the event driven messaging subsystem 240 may employ a separate event driven message generator 260 for each channel of the slave device 210, for a group of channels of the slave device 210, or for a number of slave devices 210.

The event driven messaging subsystem 240 operates as follows. The channel level detector 250 reads a level of a FIFO buffer associated with a channel of the slave device 210 and compares the level to a threshold, which may be user selectable. The threshold
5 may be set such to avoid a particular exception condition such as underflow. In this case, when the level of the FIFO buffer decreases to or below the threshold, the event driven message generator 260 will issue an event driven message 265 to the master device 220. In response, the master device 220 may transmit a
10 number of packets (transmit packets 225) to the slave device 210, which may be designated for the particular channel, to avoid the underflow condition on that channel.

In an advantageous embodiment, the channel level detector 250 may compare the level of the FIFO buffer to multiple thresholds and
15 cause the event driven message generator 260 to issue different event driven messages 265 to the master device 220 based on the threshold reached by the FIFO buffer. For example, the channel level detector may compare the level of the FIFO buffer to both an upper and a lower threshold. The event driven message generator
20 260 may then send an event driven message 265 to the master device 220 to request more transmit packets 225 when the level reaches the lower threshold, or request the master device 220 to temporarily suspend the sending of transmit packets 225 to the particular

channel when the level reaches the upper threshold. Those skilled in the pertinent art are familiar with the various exception conditions and their relationship to various threshold levels.

The event driven message may be transmitted in band, along with the data, to eliminate the need for a separate interface between the master device and the slave device. Pin counts of both the master device and the slave device may thus be advantageously reduced. The master device and the slave device may employ a Utopia-like interface, with the event driven message transmitted across a local interface between the master device and the slave device.

The periodic messaging subsystem 270 includes an aggregate level detector 280 that, in the illustrated embodiment, is associated with the slave device 210. The periodic messaging subsystem 270 further includes a periodic message generator 290, which, in the illustrated embodiment, is also associated with the slave device 210. The aggregate level detector 280 and the periodic message generator 290 may be incorporated within the slave device 210 or, alternatively, may be external to the slave device 210 and remain well within the scope of the present invention.

In the illustrated embodiment, one aggregate level detector 280 is employed per slave device 210. Of course, the slave device 210 may employ more than one aggregate level detector 280, or

multiple slave devices 210 may employ only one aggregate level detector 280 and remain well within the scope of the present invention. Regardless, the aggregate level detector 280 is configured to determine storage levels of a plurality of channels
5 associate with the slave device 210. Likewise, the periodic messaging subsystem 290 may employ one or more separate periodic message generators 290 for each slave device 210, or may, alternatively, employ only one periodic messaging subsystem 290 for a plurality of slave devices 210.

10 The periodic messaging subsystem 270 operates as follows. Periodically, the aggregate level detector 280 reads storage levels of a group of FIFO buffers associated with a corresponding group of channels (associated with packet storage buffer 212) of the slave device 210. The periodic message generator 290 then issues a
15 periodic message 295 to the master device 220, which indicates the storage levels. In the illustrated embodiment, the periodic message 295 may be embodied in a single packet to minimize an amount of traffic between the slave device 210 and the master device 220. Of course, the periodic message 295 may be in the form
20 of multiple packets and remain well within the scope of the present invention.

Upon receipt of the periodic message 295, the master device 220 may decide to transmit a number of additional packets (transmit

packets 225) or, alternatively, may refrain from transmitting any additional transmit packets 225 for a period of time. The periodic message 295 may include information pertaining to each individual channel associated with the slave device 210 to allow the master device 220 to distinguish the channel(s) that may require more transmit packets 225 from those that do not require more transmit packets 225 at a particular time. By employing the periodic messages 295, the master device 220 may regulate the amount of transmit packets 225 sent to each channel of the slave device 210, or the number of transmit packets 225 sent to the slave device 210 in general, thereby minimizing the occurrence of exception conditions such as underflow.

Those skilled in the art should understand that the previously described embodiment of the messaging system are submitted for illustrative purposes only and other embodiments are well within the scope of the present invention.

Although the present invention has been described in detail, those skilled in the art should understand that they can make various changes, substitutions and alterations herein without departing from the spirit and scope of the invention in its broadest form.